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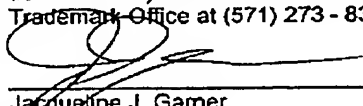
Applicant: Visokay et al. Docket No.: TI-35227
Serial No.: 10/734,708 Art Unit: 2813
Filing Date: 12/11/2003 Examiner: Thanhha S. Pham
Customer No.: 23494 Conf. No.: 2373
Title: Method for Fabricating Transistor Gate Structures and Gate Dielectrics Thereof

AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being
transmitted by facsimile to the U.S. Patent and
Trademark Office at (571) 273 - 8300 on 4/19/2006.


Jacqueline J. Garner
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 10/20/2005. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no Amendments to the Specification.

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

There are no Amendments to the Drawings.

The Remarks begin on page 9.